

FIG. 1
PRIOR ART

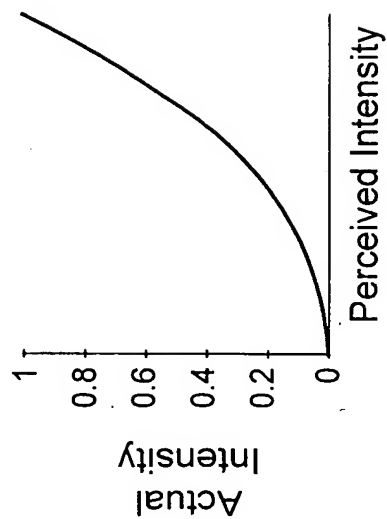


FIG. 2

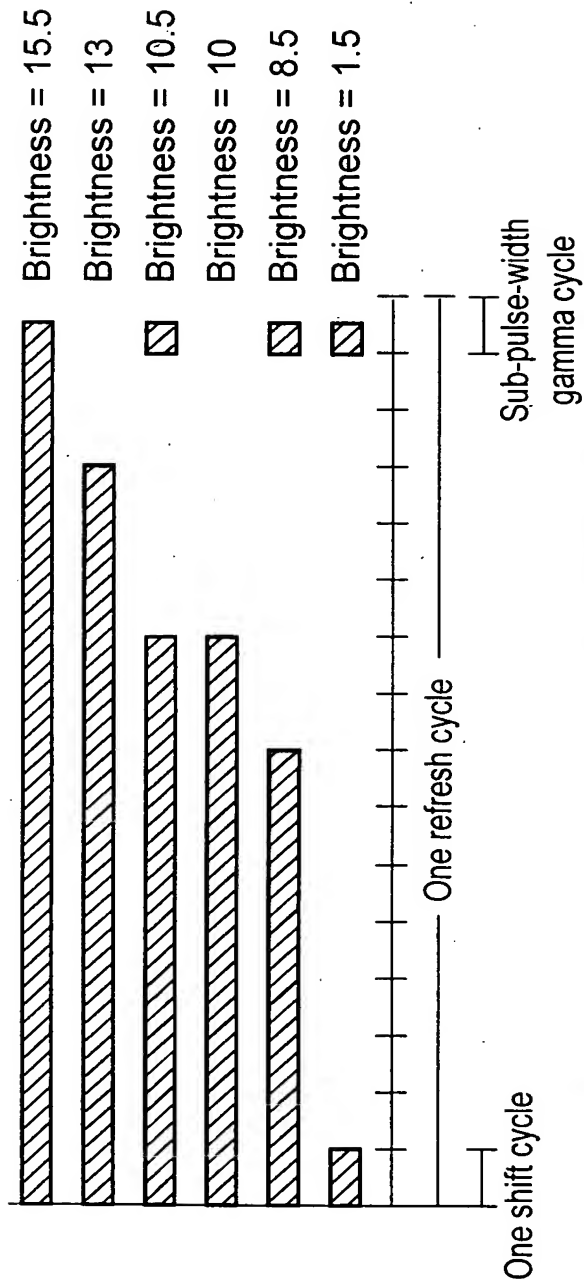


FIG. 3

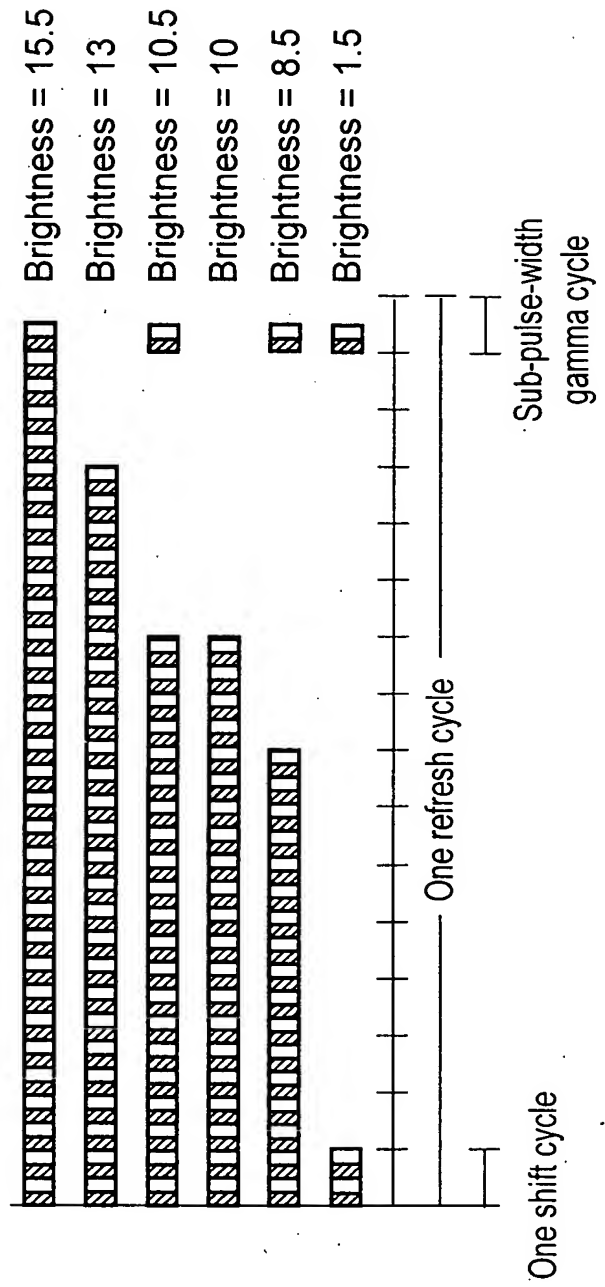


FIG. 4

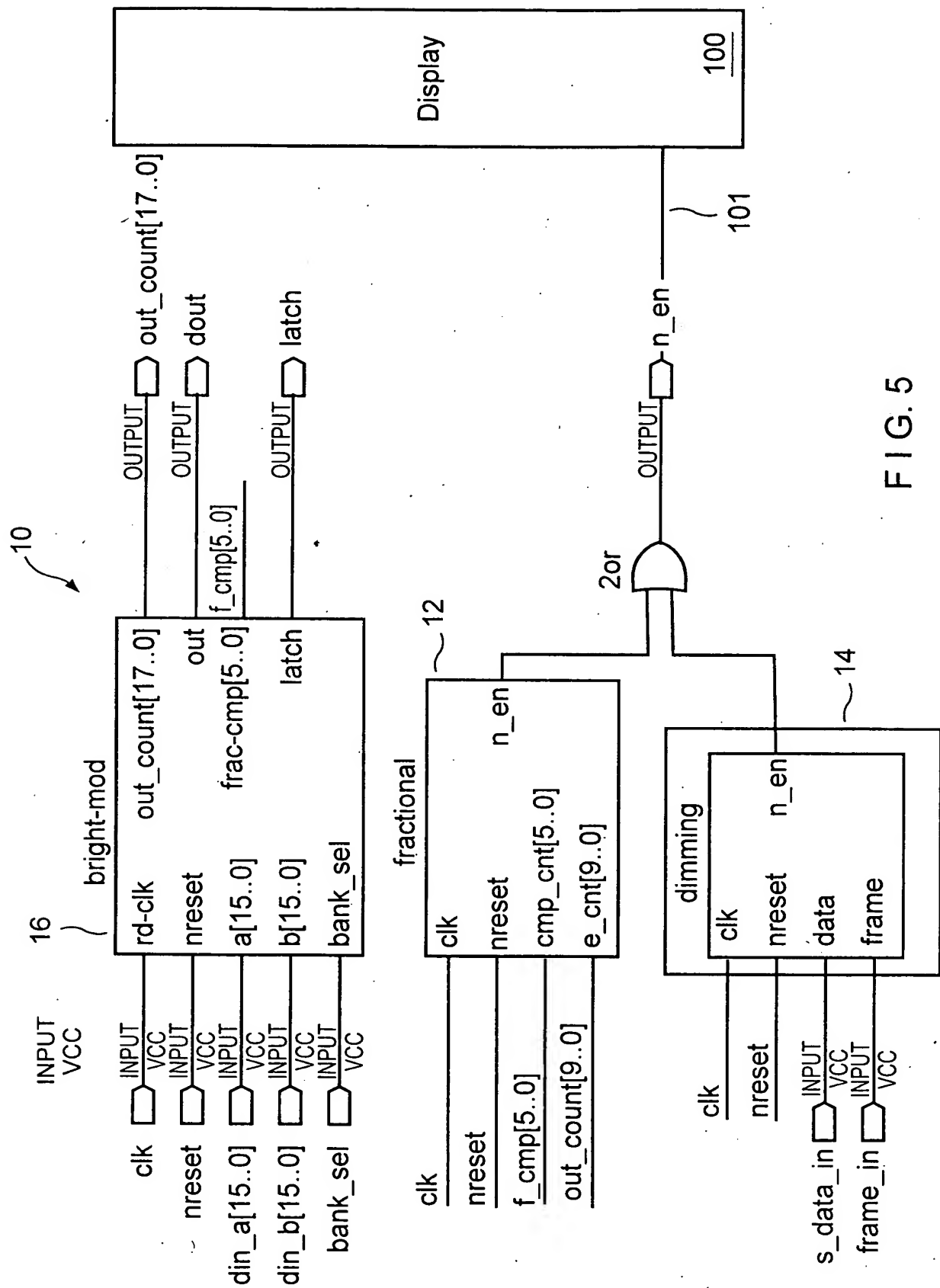
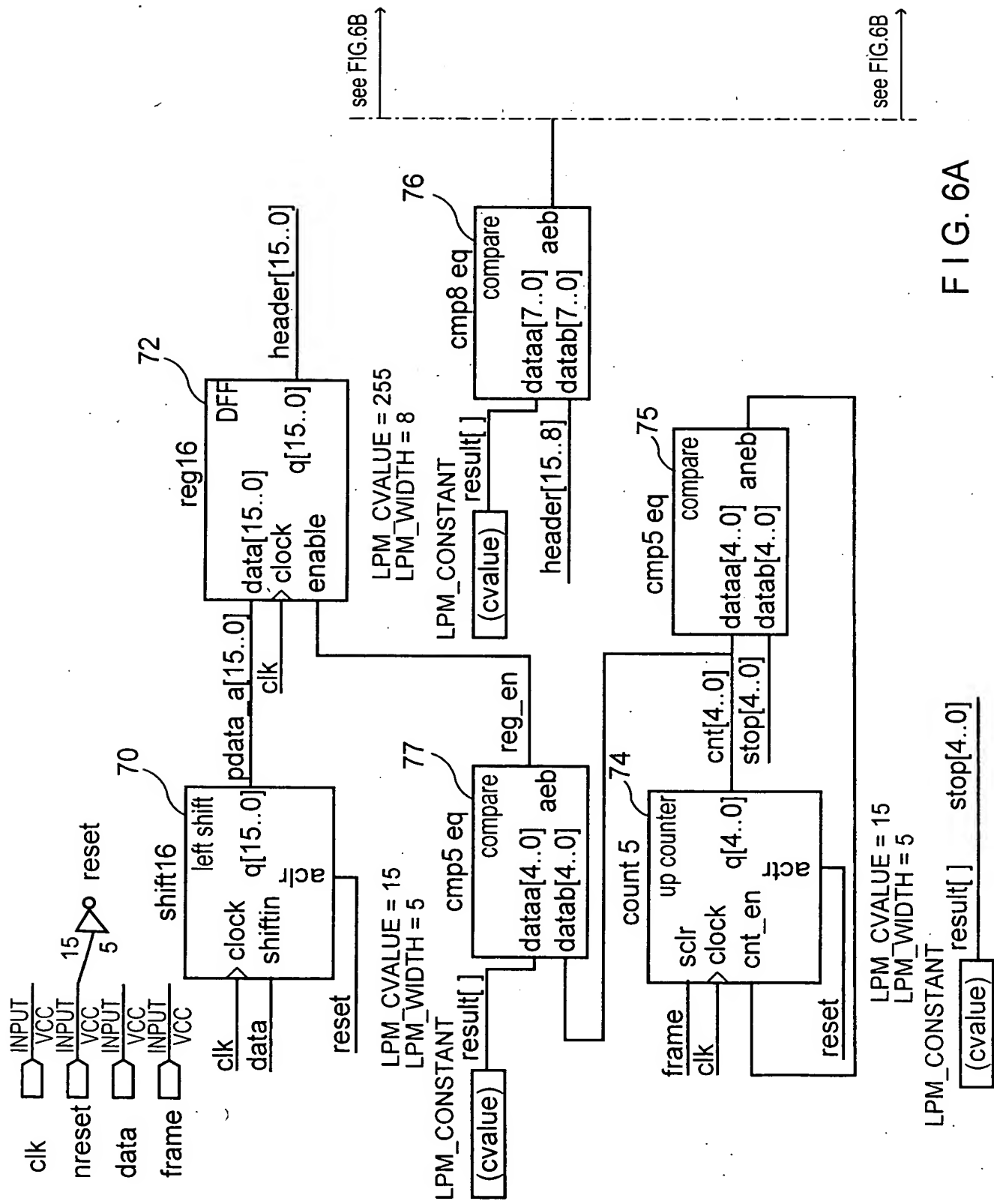


FIG. 5



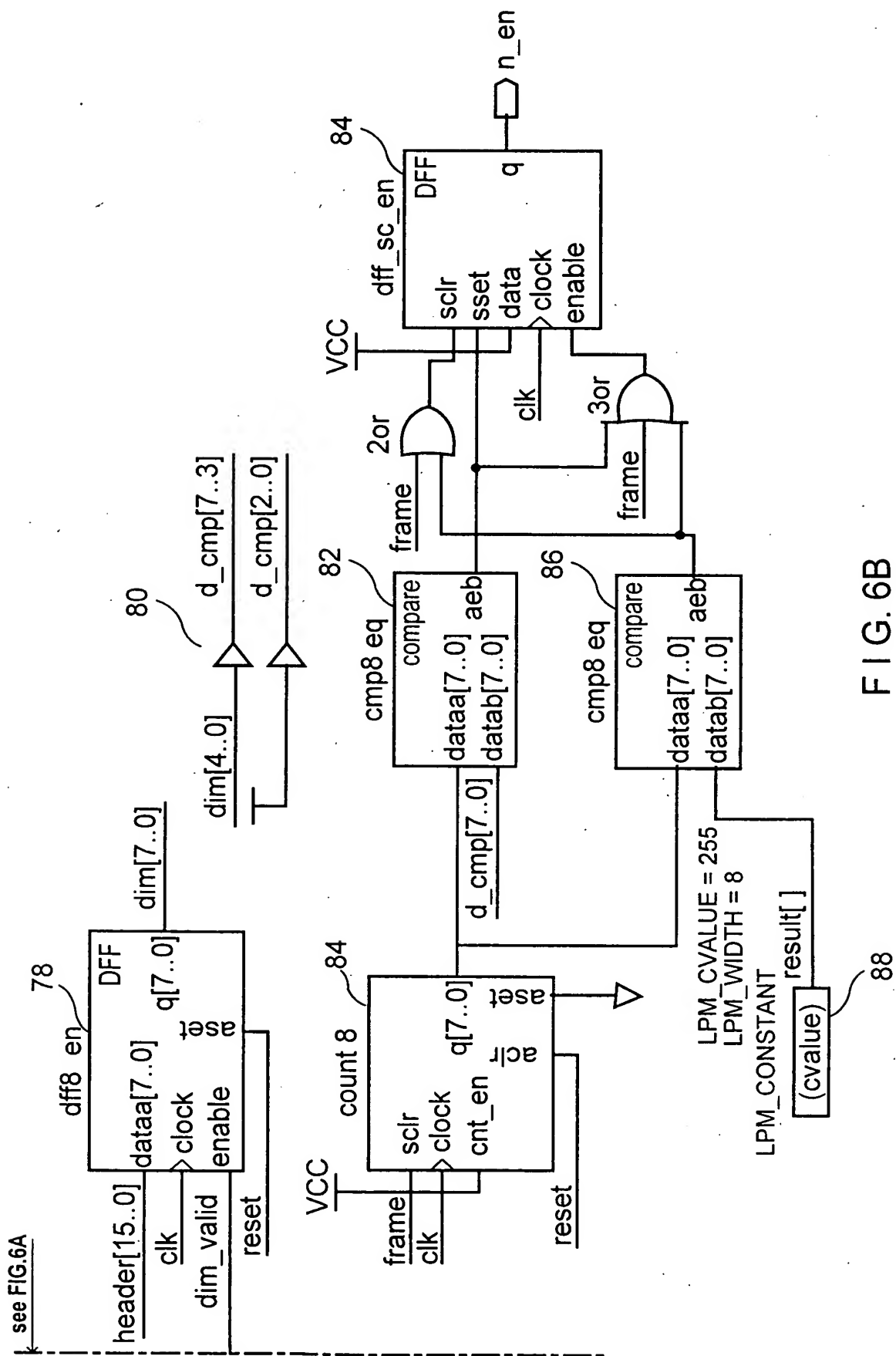
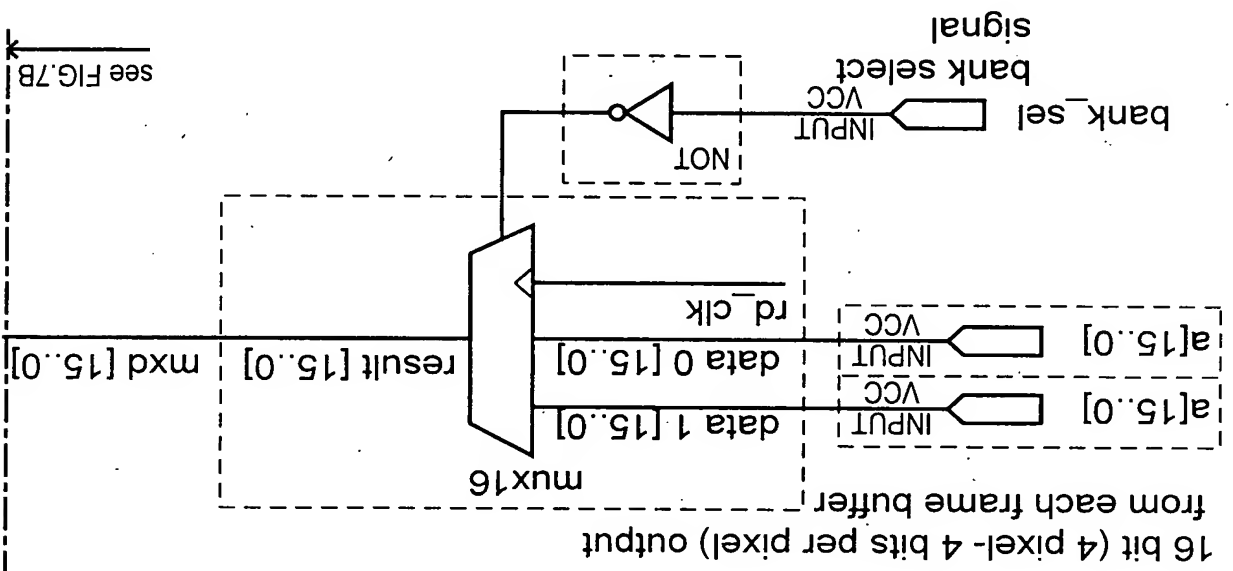
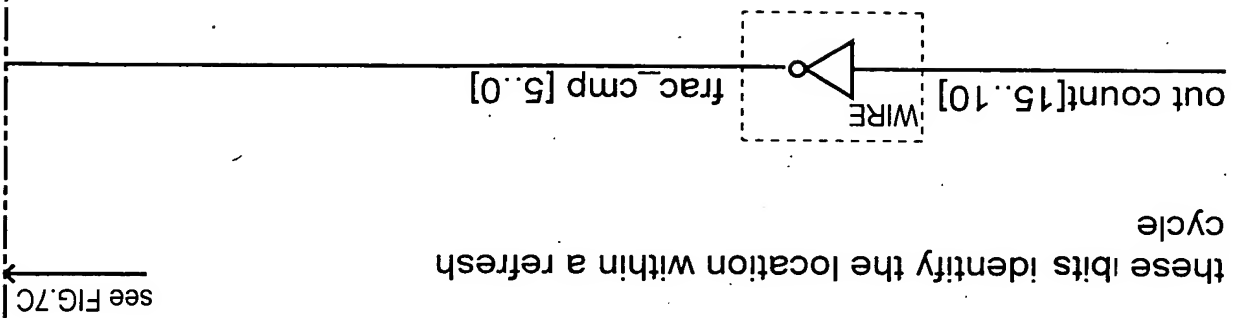


FIG. 6B

see FIG. 7C

FIG. 7A



see FIG. 7B

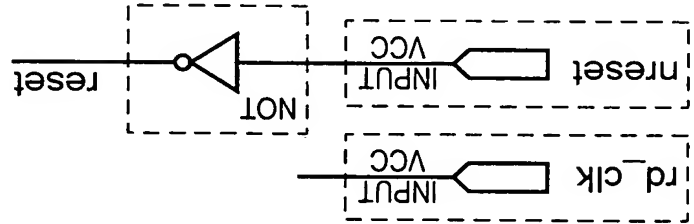
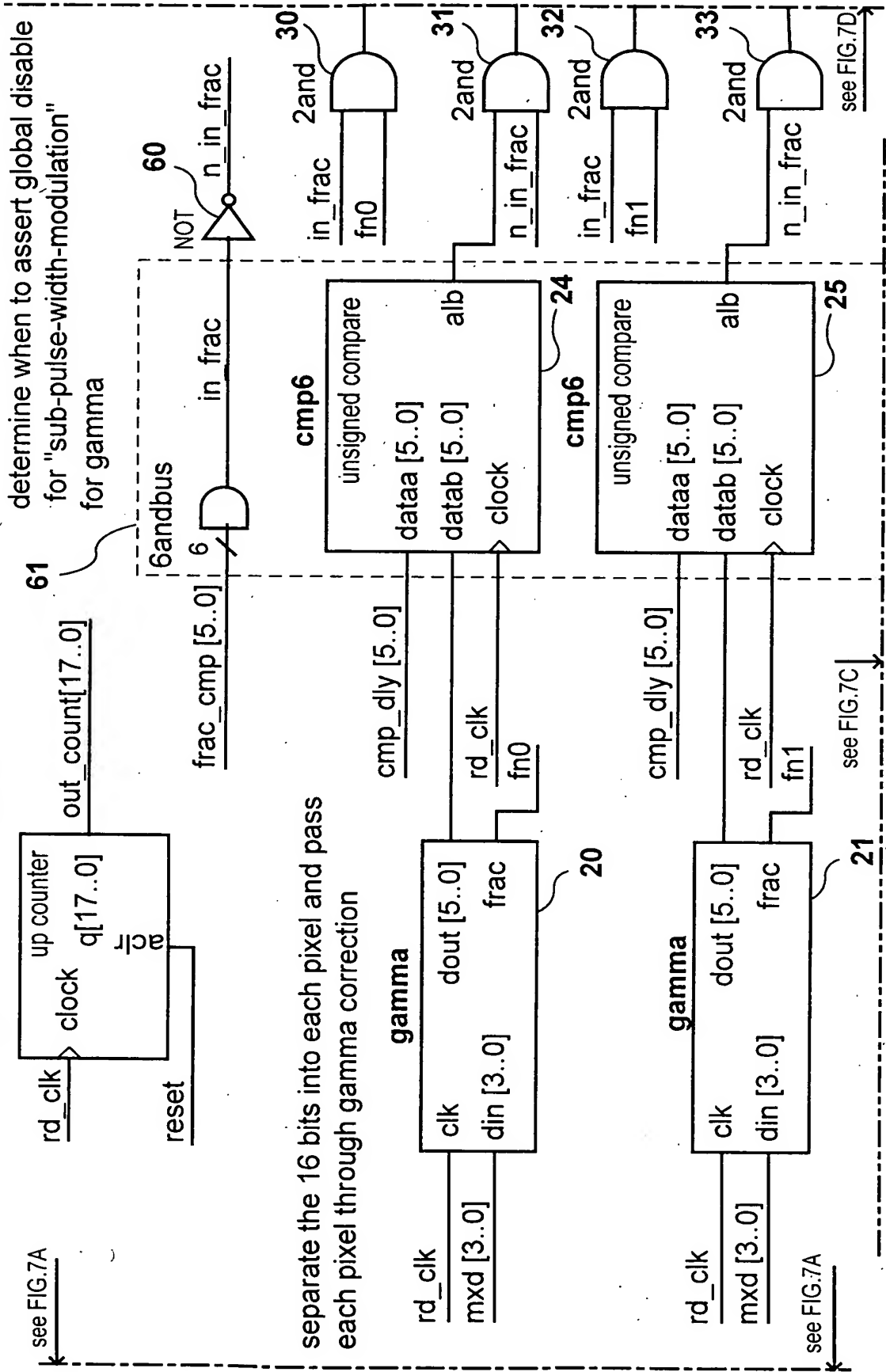


FIG. 7B

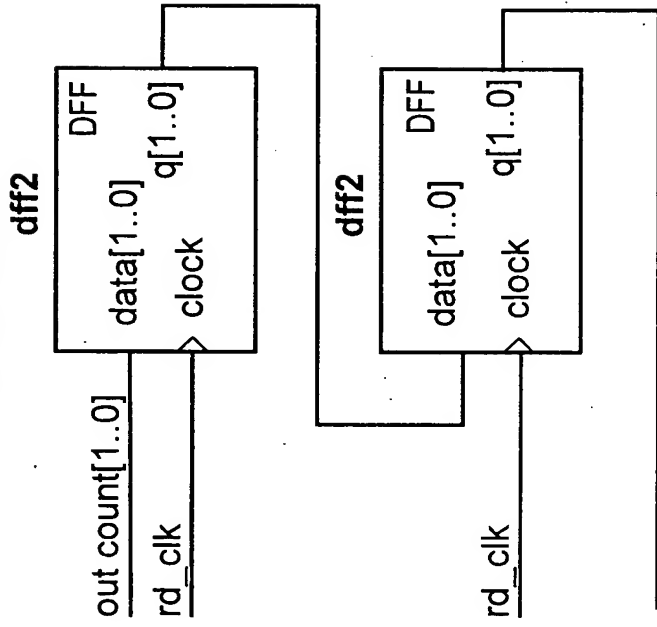
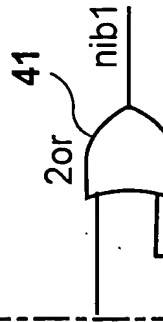
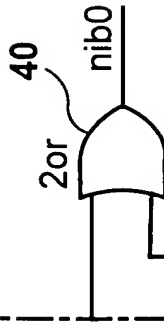
global counter to keep track
of all pixels and refresh cycles



see FIG.7B

OUTPUT  out count[17..0]

delay the count to align
with data coming out of
the compares



see FIG.7E

see FIG.7B

see FIG.7E

FIG. 7D

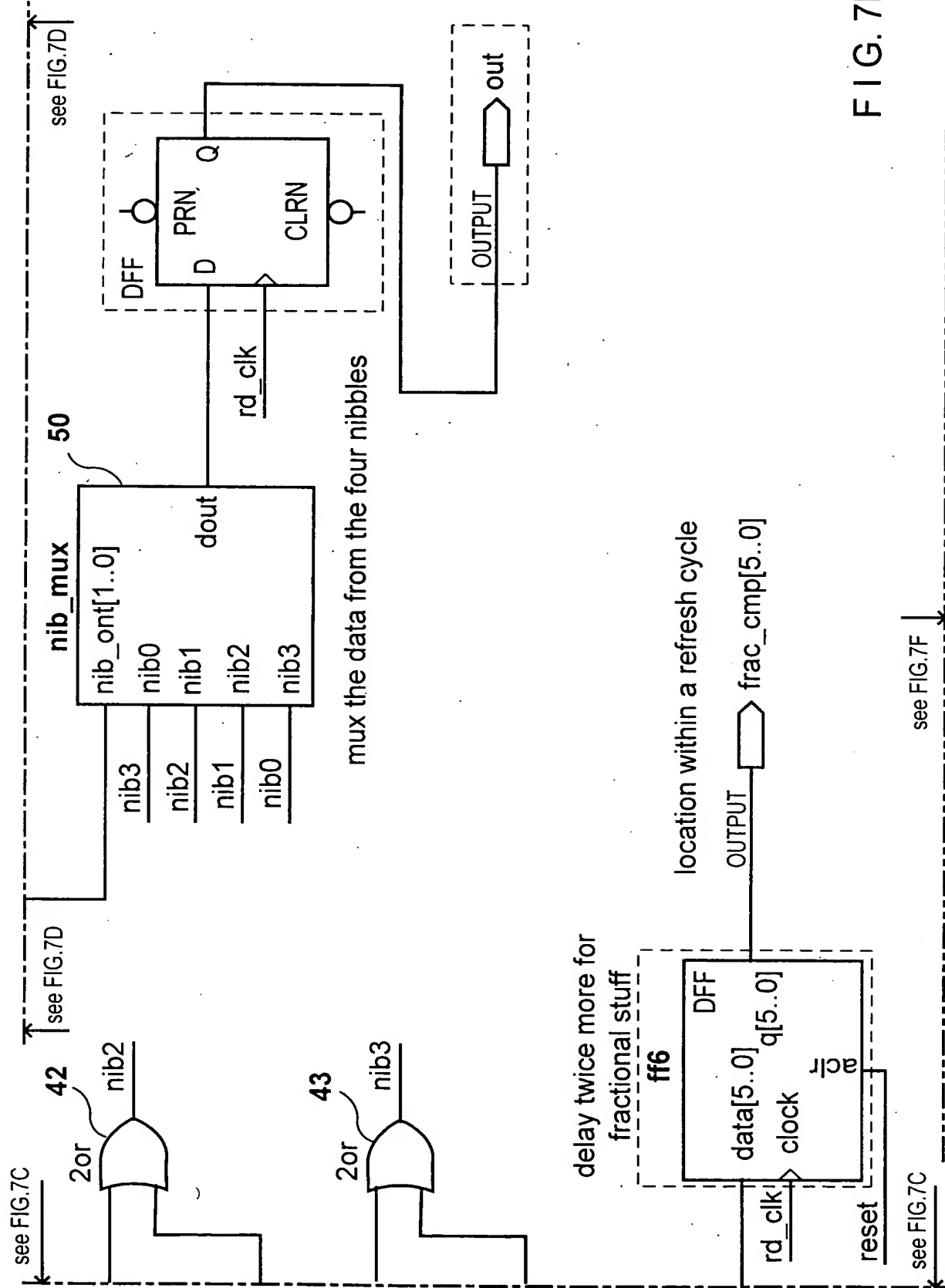


FIG. 7E

see FIG.7E

determine when latch
needs to be asserted

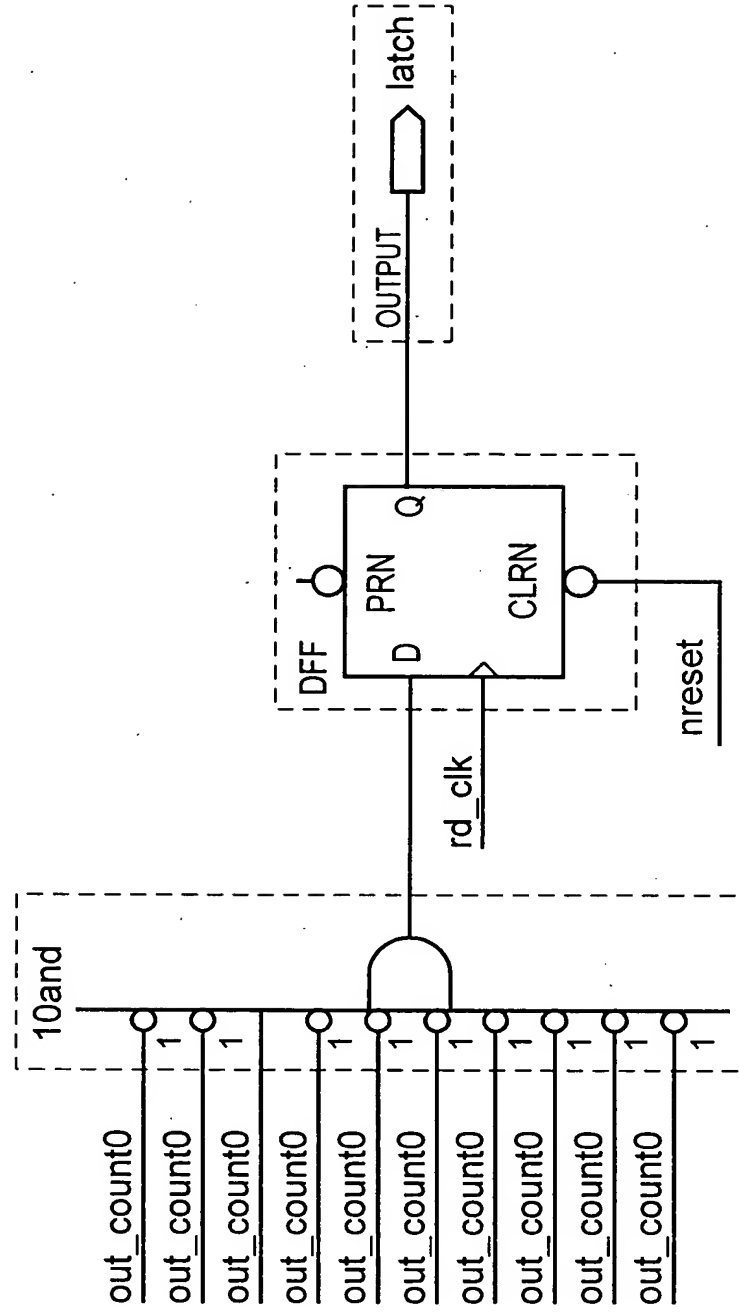


FIG. 7F

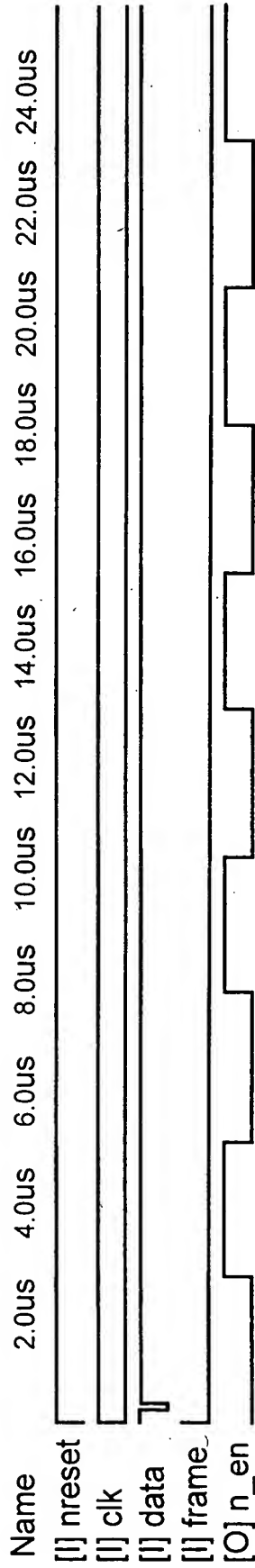


FIG. 8A

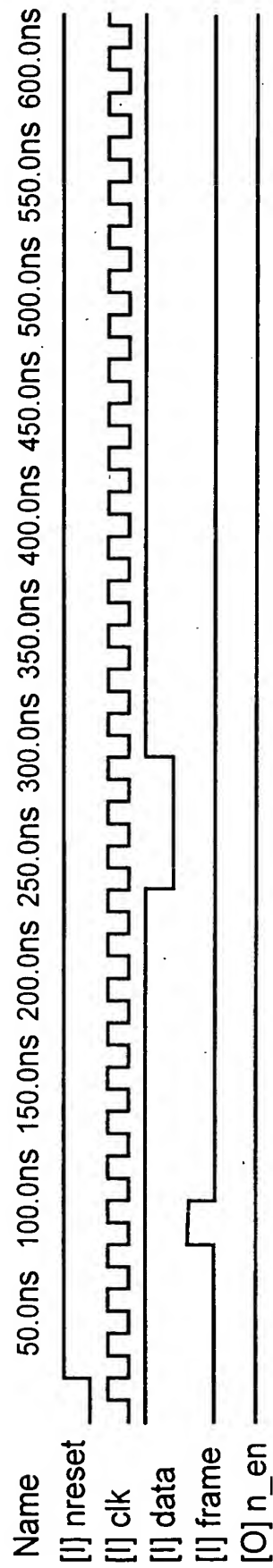


FIG. 8B

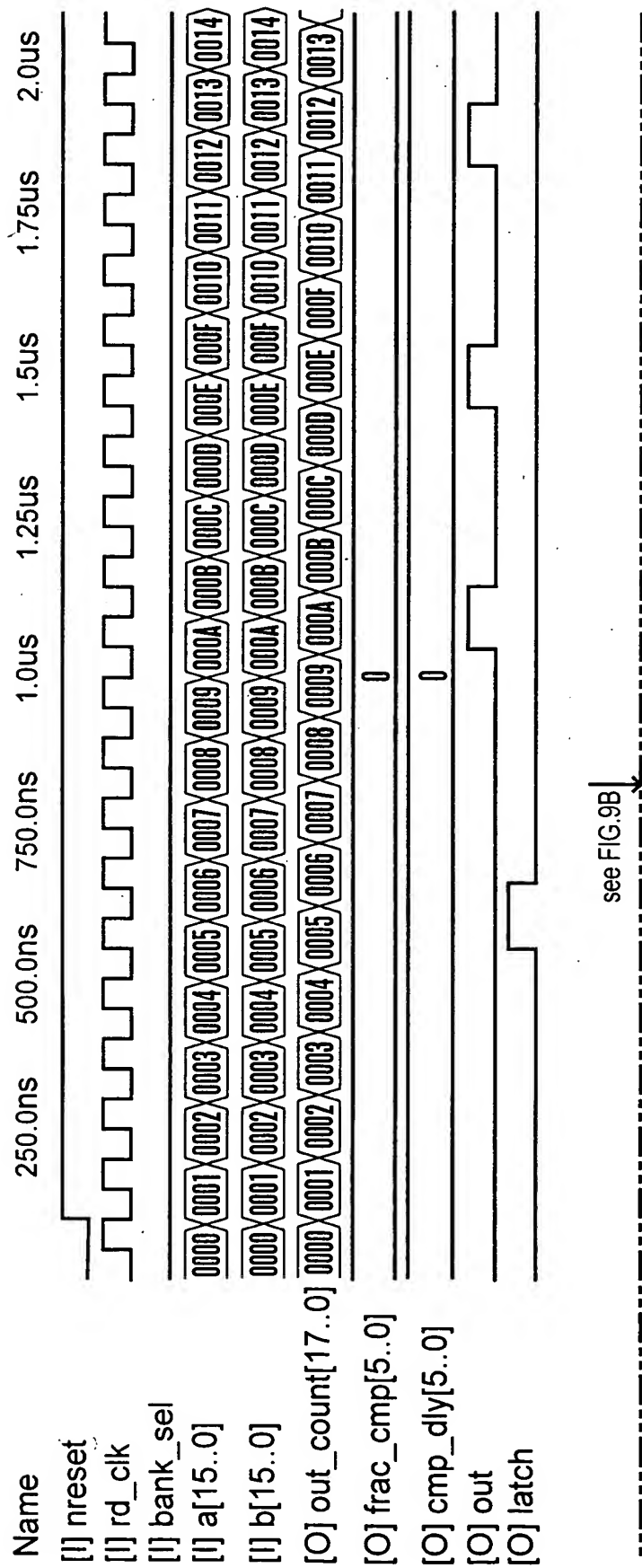
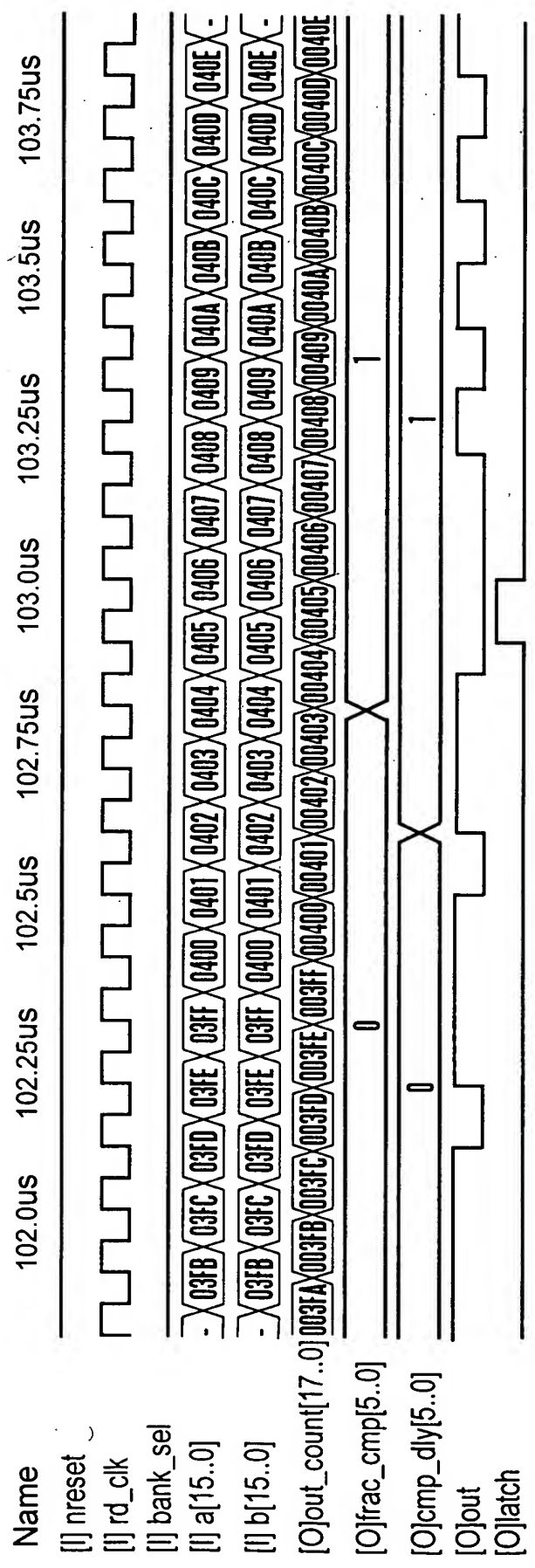


FIG. 9A

----- see FIG. 9A ↑



see FIG. 9C ↓

FIG. 9B

-----↑ see FIG.9B

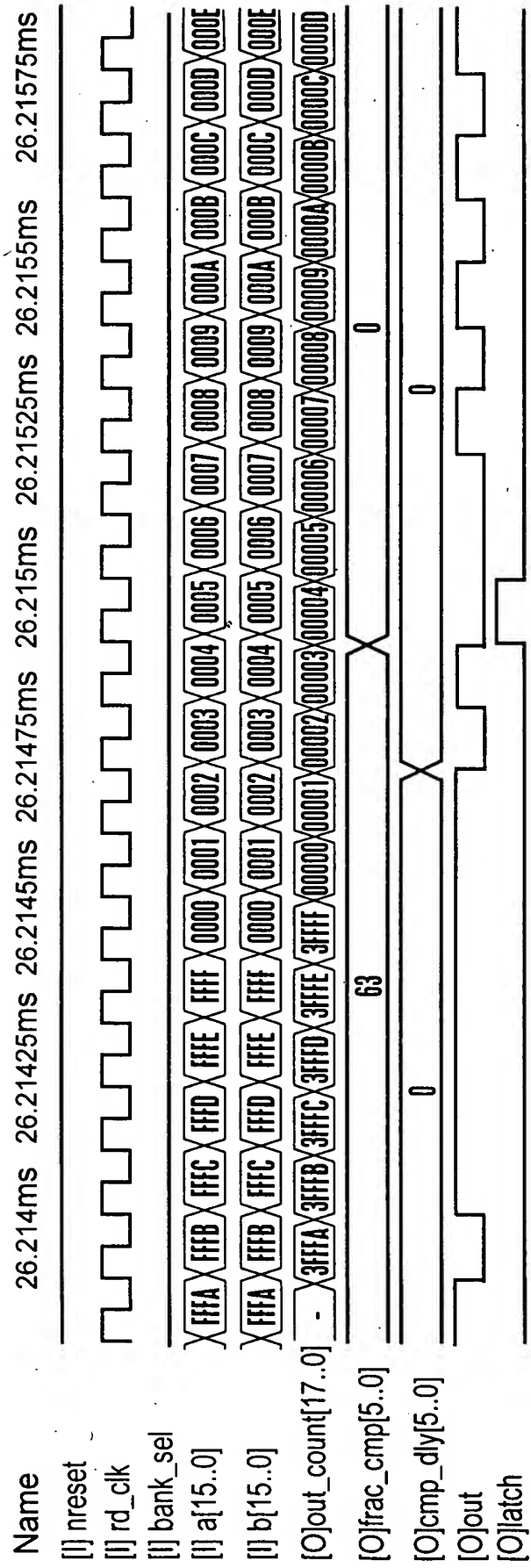


FIG. 9C